



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/671,436	09/27/2000	Yoshinari Matsuda	09792909-0425	6069
26263	7590	10/05/2006		EXAMINER
		SONNENSCHEIN NATH & ROSENTHAL LLP		LEVI, DAMEON E
		P.O. BOX 061080		
		WACKER DRIVE STATION, SEARS TOWER	ART UNIT	PAPER NUMBER
		CHICAGO, IL 60606-1080		2841

DATE MAILED: 10/05/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)
	09/671,436	MATSUDA ET AL.
	Examiner	Art Unit
	Dameon E Levi	2841

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 07/12/2006(Response).

2a) This action is FINAL.                    2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-6 and 8-20 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5) Claim(s) \_\_\_\_\_ is/are allowed.

6) Claim(s) 1-6,8-20 is/are rejected.

7) Claim(s) \_\_\_\_\_ is/are objected to.

8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 27 September 2000 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

#### Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some \* c) None of:

1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

#### Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____	6) <input type="checkbox"/> Other: _____

**DETAILED ACTION**

**NEW GROUNDS OF REJECTION**

Applicant's arguments, filed 07/12/2006, with respect to the rejection(s) of claim(s) 1-6, 8-20 have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made below.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

**Claims 1-6, and 8 are rejected under 35 U.S.C. 103(a) as being as being unpatentable over Fushie et al US Patent 6339197 in view of Yamamoto et al US Patent 6577057.**

**Regarding claim 1,** Fushie et al discloses a printed circuit board comprising: a glass substrate( element 2, Figs 1-6C, 20, Figs 7A-8B, 32, Figs 9A-12B) provided with through-holes( elements 3, Figs 1-6C) , conductive patterns( elements 6, Figs 1-6C) provided on both surfaces of the glass substrate in such a manner as to be made conductive to each other via the through-holes, and a sealing member( element 8, Figs

1-6C) provided to fill the through holes, the sealing member being operable to inhibit moisture permeation through the through holes .

Fushie et al does not expressly disclose the glass substrate having a sealed side surface facing the portion to be sealed from moisture and an exposed side surface; or, the conductive patterns on said sealed side surface being connected to at least one display element.

Yamamoto et al discloses a device comprising a glass substrate(element 21, Figs 1-7) having a sealed side surface (elements 29,41,Figs 1-7) facing a portion to be sealed from moisture and an exposed side surface(elements 23, Figs 1-7); and, the conductive patterns(elements 28,32, Figs 1-7) on said sealed side surface being connected to at least one display element( elements 11, Figs 1-7).

Accordingly, it would have been obvious to one of ordinary skill in the art, at the time the invention was made to have included a sealed side surface and an exposed side surface, as well as, to connect the conductive patterns to a display element as taught by Yamamoto et al in the display device as taught by Fushie et al for the purpose of providing a sealed in atmosphere, as well as, to electrically connect the display element to the substrate.

**Regarding claim 2**, Fushie et al discloses wherein the glass substrate is a no-alkali glass substrate( Fig 7A).

**Regarding claim 3**, Fushie et al discloses wherein the sealing member is a conductive paste containing an epoxy resin as a binder( element 8, Figs 1-6C)

**Regarding claim 4**, Fushie et al discloses wherein a conductive film is provided on an

inner wall surface of each of the through-holes in such a manner as to connect the conductive patterns provided on both surfaces of the glass substrate to each other, and an inner space, inside the conductive film, of the through-hole is filled with the sealing member ( elements 3,8 Figs 1-6C)

**Regarding claim 5**, Fushie et al discloses wherein the sealing member is an epoxy resin( elements 8, Figs 1-6C)

**Regarding claim 6**, Fushie et al discloses wherein the surface of the sealing member exposed from each of the through-holes is covered with a metal film ( elements 3,8 Figs 1-6C)

**Regarding claim 8**, Fushie et al discloses wherein each of said conductive patterns has a stacked structure of an epoxy resin film and a copper film formed thereon ( element 35a, 35b, 35c, Figs 1-14).

**Claims 9-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fushie et al US Patent 6339197 in view of Yamamoto et al US Patent 6577057, Stevens US Patent 6392356 and further in view of Nakazawa et al US Patent 6411349**

**Regarding claim 9**, Fushie et al discloses a device comprising:  
a printed wiring board including a glass substrate( element 2, Figs 1-6C, 20, Figs 7A-8B, 32, Figs 9A-12B) provided with through-holes( elements 3, Figs 1-6C),  
conductive patterns( elements 6, Figs 1-6C) provided on both surfaces of the glass substrate in such a manner as to be made conductive to each other via the through

holes, and a first sealing member( element 8, Figs 1-6C) provided to fill the through-holes;

Yamamoto et al discloses a device comprising a glass substrate(element 21, Figs 1-7) having a sealed side surface (element 29,41 Figs 1-7) facing a portion to be sealed from moisture and an exposed side surface(element 23, Figs 1-7); and, the conductive patterns(elements 28,32, Figs 1-7) on said sealed side surface being connected to at least one display element(elements 11, Figs 1-7).

Stevens et al discloses a display device assembly comprising

- a display device provided on one surface of the printed wiring board in such a manner as to be connected to a conductive pattern provided on a one surface of a printed wiring board( elements 30, Fig 3)
- a drive component for driving the display device, the drive component being disposed on the exposed surface of the printed wiring board in such a manner as to be connected to the conductive pattern provided on the other surface of the printed wiring board( elements 70,72, Figs 1-3, see column 5, lines 5-25)

Nakazawa et al discloses a display device assembly wherein

- a second sealing member provided in such a manner as to surround a display device while being in contact with a printed wiring board and a protective glass board ( element 252, Fig 12)

Accordingly, it would have been obvious to one of ordinary skill in the art at the time the

invention was made to have included the glass circuit board with a sealed side surface and an exposed side surface and to connect the conductive patterns to a display element as taught by Fushie et al and Yamamoto et al for the purpose of providing a sealed in atmosphere, as well as, to electrically connect the display element to the substrate and also to arrange the display device components as taught by Stevens for the purpose of achieving a denser array of driver components in order to increase pixel pitch in the display device and additionally to add the second sealing member as taught by Nakazawa et al for the purpose vacuum sealing the assembly as a whole.

**Regarding claim 10**, Fushie et al disclose the instant wherein the glass substrate is a no-alkali glass substrate( Fig 7A)

**Regarding claim 11**, Fushie et al discloses wherein the sealing member is a conductive paste containing an epoxy resin as a binder ( element 8, Figs 1-6C)

**Regarding claim 12**, Fushie et al discloses wherein a conductive film is provided on an inner wall surface of each of the through-holes in such a manner as to connect the conductive patterns provided on both surfaces of the glass substrate to each other, and an inner space, inside the conductive film, of the through-hole is filled with the sealing member ( elements 3,8 Figs 1-6C)

**Regarding claim 13**, Fushie et al discloses wherein the sealing member is an epoxy resin( elements 8, Figs 1-6C)

**Regarding claim 14**, Fushie et al discloses wherein the surface of the sealing member exposed from each of the through-holes is covered with a metal film ( elements 3,8 Figs 1-6C)

**Regarding claim 15, Fushie et al discloses a device comprising:**

- a printed wiring board including a glass substrate( element 2, Figs 1-6C, 20, Figs 7A-8B, 32, Figs 9A-12B) provided with through-holes( elements 3, Figs 1-6C), conductive patterns ( elements 6, Figs 1-6C) provided on both surfaces of the glass substrate in such a manner as to be made conductive to each other via the through holes, and a first sealing member( element 8, Figs 1-6C) provided to fill the through-holes;

**Yamamoto et al discloses a device comprising**

- a glass substrate ( elements 21, Figs 1-7)having a sealed side surface ( elements 29, 41,Figs 1-7) facing a portion to be sealed from moisture and an exposed side surface( elements 23, Figs 1-7); and, the conductive patterns( elements 28,32Figs 1-7) on said sealed side surface being connected to at least one display element( elements 11, Figs 1-7).

**Stevens et al discloses a display device assembly comprising**

- bumps provided on a conductive pattern provided on one surface of a printed wiring board; a protective glass board disposed in such a manner as to face to the one surface of the printed wiring board; a display device provided on the surface, facing to the printed wiring board, of the protective glass board in such a manner as to be connected to the bumps( elements 60,12,30 Fig 3)
- a drive component for driving the display device, the drive component being disposed on the exposed surface of the printed wiring board in such a manner as

to be connected to the conductive pattern provided on the other surface of the printed wiring board( elements 70,72, Figs 1-3, see column 5, lines 5-25)

Nakazawa et al discloses a display device assembly wherein:

- a second sealing member is provided in such a manner as to surround a display device while being in contact with a printed wiring board and a protective glass board ( element 252, fig 12)

Accordingly, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have included the glass circuit board with a sealed side surface and an exposed side surface and to connect the conductive patterns to a display element as taught by Fushie et al and Yamamoto et al for the purpose of providing a sealed in atmosphere, as well as, to electrically connect the display element to the substrate and also to arrange the display device components as taught by Stevens for the purpose of achieving a denser array of driver components in order to increase pixel pitch in the display device and additionally to add the second sealing member as taught by Nakazawa et al for the purpose vacuum sealing the assembly as a whole.

**Regarding claim 16**, Fushie et al disclose the instant wherein the glass substrate is a no-alkali glass substrate ( Fig 7A)

**Regarding claim 17**, Fushie et al discloses wherein a conductive film is provided on an inner wall surface of each of the through-holes( elements 3,8 Figs 1-6C) in such a manner as to connect the conductive patterns provided on both surfaces of the glass substrate to each other, and an inner space( elements 8 Figs 1-6C), inside the conductive film, of the through-hole is filled with the sealing member.

**Regarding claim 18,** Fushie et al discloses wherein the sealing member is an epoxy resin( elements 8, Figs 1-6C)

**Regarding claim 19,** Fushie et al discloses wherein the surface of the sealing member exposed from each of the through-holes is covered with a metal film ( elements 3,8 Figs 1-6C)

Regarding claim 20, Fushie et al discloses wherein the surface of the sealing member exposed from each of first sealing member exposed from each of the through-holes is covered with a metal film (elements 5c, Figs 4D).

***Response to Arguments***

Applicant's arguments, with respect to the rejection(s) of claim(s) 1-6, and 8-20 have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made above.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dameon E. Levi whose telephone number is (571) 272-2105. The examiner can normally be reached on Mon.-Fri. (9:00 - 5:00) IFP.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Elvin Enad can be reached on (571) 272-1990. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

DEL

  
9-29-06,  


Dameon E Levi  
Examiner  
Art Unit 2841